

**12th CONFERENCE – SEMINAR
INTERNATIONAL SCHOOL ON NONSINUSOIDAL
CURRENTS AND COMPENSATION**

ISNCC 2015

15-18 June 2015

Łagów, Poland

PROGRAM

www.ISNCC2015.ime.uz.zgora.pl

University of Zielona Góra
Institute of Electrical Metrology
Institute of Electrical Engineering

Conference Venue

The event will be held at the OW “Leśnik” (Holiday House “Leśnik”) in Łagów, Chrobrego Street,10. Holiday House “Leśnik” is located in the centre of Łagów.

Conference Registration

Registration desk will be located on the ground floor of Holiday House “Leśnik”. The conference registration desk will be open as follows:

Monday, 15 June 2015, 3:30 PM – 9:30 PM

Tuesday, 16 June 2015, 7:30 AM – 7:00 PM

Conference participants who have registered can pick up their materials, badges and other items from the registration desk.

All conference participants are issued with a personal badge showing their name and affiliation. Please observe that for security reasons the badge must be worn at all times during the conference and the social events. Access may be denied for participants not wearing their badge.

Hotel Information

Conference participants will be accommodated in the Holiday House “Leśnik” or “Zamek Joannitów” (Joannitów Castle). The Joannitów Castle is located 4 minutes walking distance from the Holiday House “Leśnik”.

Meals

Breakfast will be served each day 8.00 AM – 9.00 PM in the Holiday House “Leśnik” or Joannitów Castle, depending on accommodation place.

Meals (snacks in the coffee breaks, dinners, supper), for all conference participants, will be served in the dining room located in the ground floor of the Holiday House “Leśnik”.

Welcome Party

A welcome party will be held on Tuesday, June 16, from 7:30 PM in the summer pavilion of Holiday House "Leśnik". There is a picnic place with access to the lake. We have planned a grill supper with good food (among the others roasted wild boar) and beer. All participants and their companion are welcome to attend.

Gala Dinner

Our gala dinner will take place on Wednesday, June 17, in the courtyard of the Joannitów Castle. We have planned an enjoyable evening with good food and wine. We hope to see you all at that time.

Castle tower

On the tower of "Joannitow Castle" is a vantage point. All participants of the Conference can enter the tower without charge on Wednesday, June 17 from 6.00 PM to 8.00 PM.

Oral Presentations

Oral presentation should not last longer than 15 minutes. After presentation 5 minutes for questions and answers has been scheduled.

Show up at least 10 minutes before the session starts and meet the session chairman so you have the possibility to inspect the lecture hall and its facilities (computers and digital projectors). At the session speak simple as possible and stick to the time limit so questions can be raised.

During the presentation a laptop, with installed MS Power Point 2007 and Acrobat Reader, and a projector will be available for each speaker. If you are not sure that your presentation will be reproduced properly, find in your tool an option which allows you to make your presentation portable.

Tutorial T1:

Single - Point Methods for Location of Electromagnetic Disturbances in Power System

Author: **Zbigniew HANZELKA**, AGH-University of Science and Technology, Poland.

Often, in the case of a significant level of electromagnetic disturbance in electrical power system, at the customer's supply terminals, there is a need for locating the source of this disturbance, e.g. harmonics, voltage fluctuations, voltage dips, occasionally also asymmetry. Utilities and power consumers have become increasingly interested in quantifying the responsibilities for power quality problems. This issue gains particular meaning when formulating contracts for electric power supply and enforcing, by means of tariff rates, extra charges for worsening the power quality. This paper concerns as an example an application of one method (from the collection of many existing) used to locate sources of electromagnetic disturbances (harmonics, dips, voltage fluctuations and asymmetry), based on the study of the power flow at the point of common coupling (PCC).

Tutorial T2:

Power Quality in Low-Voltage Distribution Network with Distributed Generation

Author: **Tomasz SIKORSKI**, Wroclaw University of Technology, Poland.

The aim of this work is to combine electromagnetic compatibility standardization and control strategy of low-voltage distributed generations in order to define possible impact of the generation unit on power quality at the point of common coupling with low-voltage distribution network. Selected relations between power quality indices and parameters as well as regulation characteristic of the distributed generation is discussed. Verification of predicted impact of the distributed generation on power quality is performed using a field-measurement case study of power quality behaviour in real photovoltaic system connected to low-voltage distribution network..

Tutorial T3:

A Sparse Sampling Approach to Dynamic Sub-Cycle Decomposition of Apparent Power in General Polyphase Networks

Authors: **Hanoch LEV-ARI**, Northeastern University, USA,,
Aleksandar M. STANKOVIC, Tufts University, USA.

We present a dynamic decomposition of apparent power in a polyphase network, based on measurements taken within a portion of a single cycle of the ac fundamental. We show that two time samples suffice for a definition that provides an attractive trade-off between computational cost and dynamic performance. The sub-cycle (or near-instantaneous) metrics that we introduce connect the time- and frequency–domain approaches in a natural way, and address the key weaknesses of the existing instantaneous decompositions. Furthermore, our power metrics are amenable to refinements via symmetrical components, thus connecting with concepts commonly used in system protection. We illustrate the sub-cycle decomposition via two polyphase examples: one of conceptual interest, the other an actual industrial voltage sag event.

Tutorial T4:

Currents' Physical Components (CPC) in systems with semi-periodic voltages and currents

Author: **Leszek S. CZARNECKI**, School of Electrical Engineering and Computer Sciences, USA.

*There are situations in electrical distribution systems where voltages and currents cannot be regarded as periodic quantities. They could be non-periodic. Nonetheless, properties of electrical systems confine this non-periodicity of voltages and currents to a particular sub-set of non-periodic quantities, referred to as **semi-periodic** quantities in this paper. The paper presents the concept of semi-periodic quantities and defines major functionals, such as the running active power, the running rms value, the scalar product and the running complex rms (crms) value of quasi-harmonics, needed for describing electrical systems with such voltages and currents in power terms. A recursive approach to calculation of these functionals was presented as well. The paper presents fundamentals of the Currents' Physical Components (CPC) – based power theory of systems with semi-periodic voltages and currents. An application of*

the semi-periodic concept to a load current decomposition in single-phase circuits with linear loads and with harmonics generating loads is presented as well. The paper presents also a concept of extrapolation of CPC into the closest future, which enables quasi-instantaneous generation of control signals for switching compensators.

Tutorial T5:

Reference Signal Generators for Distributed Compensation

Author: **Herbert L. GINN III**, University of South Carolina, USA.

Various methods are used to generate the control reference signals for power electronics based compensators in three-phase distribution systems. In cases where increased flexibility and sharing duties across multiple compensators provides a cost benefit, the applicability of reference signal generation methods for distributed compensation applications should be evaluated. Shared compensation requires an understanding of power components that are present to enable priority based decisions with respect to the sharing. This paper provides an overview of several reference signal generation methods for power electronic converters when used in a cooperative fashion for sharing compensation duties within an electrical grid.

Tutorial T6:

AC Voltage Transforming Circuits in Power Systems

Authors: **Jacek Kaniewski, Zbigniew Fedyczak, Paweł Szcześniak**, University of Zielona Góra, Poland.

The paper deals with AC voltage transforming circuits applied in power systems. It includes a general description of AC power systems, single and three-phase AC converters, especially PWM AC line choppers and a description of their implementation in AC transmission or distribution systems. This includes a description of the topologies, the operation and test results of the voltage sag/swell compensators, quadrature phase shifters, power flow controllers, static VAR compensators and Interfaces of renewable energy sources.

Tuesday, 16th June 2015

8:00 AM – 9:00 AM	Breakfast
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9:00 AM – 9:30 AM	Welcome Addresses	Location: "Hall 1"
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Welcome address from the ISNCC 2015 chairpersons
Prof. L.S. Czarnecki, Louisiana State University, USA
The conference opening ceremony.

9:30 AM – 11:00 AM	Tutorial 1, Tutorial 2	Location: "Hall 1"
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Chairman: : Aleksandar M. STANKOVIC, Tufts University, USA

Author: Zbigniew HANZELKA, AGH-University of Science and Technology, Poland.

Single - Point Methods for Location of Electromagnetic Disturbances in Power System

Author: Tomasz SIKORSKI, Wroclaw University of Technology, Poland.

Power Quality in Low-Voltage Distribution Network with Distributed Generation

11:00 AM – 11:30 AM	Coffee Break
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11:30 AM – 1:30 PM	Lecture Session S1	Location: "Hall 1"
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Chairman: Herbert L. GINN III, University of South Carolina, USA.

ID13 ***Influence of Shunt Compensation with SVC Devices on Resonance Risk in Power Systems***

R. Kowalak, R. Malkowski, S. Czapp, J. Klucznik, Z. Lubośny, K. Dobrzyński, Gdansk University of Technology, Poland.

ID45 ***Comprehensive compensation of grid current distortion by shunt active power filters***

A. Cichowski, W. Śleszyński, J. Nieznański, Faculty of Electrical and Control Engineering, Gdansk University of Technology, Gdansk, Poland.

ID46 ***Reactive Compensation of LTI Loads in Three-Wire Systems at Asymmetrical Voltage***

Leszek S. Czarnecki, Prashanna D. Bhattarai,

School of Electrical Engineering and Computer Science, Louisiana State University, Baton Rouge, USA.

- ID24 ***Voltage and frequency regulation of a standalone induction generator by reduced-rating power electronic compensators - comparative evaluation***
M. Dempc, P.J. Chrzan, Gdansk University of Technology, Poland.
- ID25 ***Different approaches for designing the passive power filters***
Chamberlin Stéphane Azebaze Mboving, Zbigniew Hanzelka, AGH - University of Science and Technology, Poland.
- ID38 ***Methodology for Defining Effective Power Factor Compensation in Three-phase Systems***
A.C. Moreira¹, S. M. Deckmann¹, F.P. Marafão², L.C.P. da Silva¹, H.K.M. Paredes²
¹School of Electrical and Computer Engineering, University of Campinas, Campinas - SP, Brazil
²Unesp – Univ Estadual Paulista, Group of Automation and Integrated Systems, Sorocaba - SP, Brazil.

1:30 PM – 2:30 PM	Dinner
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2:30 PM – 4:15 PM	Lecture Session S2	Location: "Hall 1"
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- Chairman: Tomasz SIKORSKI, Wroclaw University of Technology, Poland
- ID11 ***Computer System for Evaluation of Shock Hazard in Circuits with Non-sinusoidal Earth Currents***
S. Czapp, D. Świsulski,
Gdansk University of Technology, Poland.
- ID31 ***Solid-state switch for capacitors bank used in reactive power compensation***
Adam Ruszczyk¹, Krzysztof Kóska¹, Konrad Janisz²
¹ABB Corporate Research Center, Krakow, Poland,
²AGH University of Science and Technology, Krakow, Poland.
- ID33 ***Modelling of voltage transfer function of the three-phase hybrid transformers with voltage or current source matrix converter***
Z. Fedyczak, P. Szcześniak, J. Kaniewski,
University of Zielona Góra, Poland.
- ID52 ***Reaction of grid-connected converters to unbalanced grid voltages with focus on modular multilevel converters***
V. Staudt¹, M. Kleine Jäger¹, D. Meyer², R. Bartelt², C. Heising²
¹Ruhr-University Bochum, Bochum, Germany,

²Avasition GmbH, Dortmund, Germany.

ID53

Blanking time effects in the context of modular multilevel converters

V. Staudt, A. Rothstein, M. Kleine Jäger, **Ruhr-University Bochum, Bochum, Germany.**

4:15 PM – 4:45 PM

Coffee Break

4:45 PM – 6:45 PM

Lecture Session S3

Location: "Hall 1"

Chairman: Volker STAUDT, Ruhr-University Bochum, Germany.

ID14

Uncertainty Estimation in Noninvasive Measurement of Harmonic Impedance –Laboratory Studies

D. Borkowski, A. Wetula, J. Kowalski, University of Science and Technology AGH, Poland

ID17

Features Generation by Means of Currents' Physical Components for Load Identification

Y. Beck¹, N. Calamero², L. Katzir², D. Shmilovitz² ¹FACULTY OF ENGINEERING, HOLON INSTITUTE OF TECHNOLOGY, HOLON, ISRAEL, ²FUCULTY OF ENGINEERING, TEL-AVIV UNIVERSITY, TEL AVIV, ISRAEL.

ID34

Numerical evaluation of the effects of phase admittances asymmetry at HVAC overhead lines

A. Pana, A. Baloi, F. Molnar-Matei, Politehnica University of Timisoara, Romania.

ID37

Supraharmonics: Concepts and Experimental Results on Photovoltaic Systems

Gianfranco Chicco, Angela Russo, Filippo Spertino, Politecnico di Torino, Energy Department, Torino, Italy.

ID15

Influence of grid-connected solar inverters and mains monitoring systems on the spectral grid impedance

G. Kaatz¹, F.Grumm², M. Jordan¹, D. Schulz¹,
¹Helmut Schmidt University / University of the Federal Armed Forces Hamburg, Germany,
²Leuphana University of Lüneburg, Lüneburg, Germany.

ID49

Estimation of parameters for obnoxious load which does not increase amplitude of voltage fluctuations

M. Michalski, G. Wiczyński, Poznań University of Technology, Poznań, Poland.

7:30 PM – 11:00 PM

Welcome Party (grill)

Location: Summer pavilion of Holiday House "Lesnik"

Wednesday, 17th June 2015

8:00 AM – 9:00 AM	Breakfast
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9:00 AM – 10:30 AM	Tutorial 3, Tutorial 4	Location: "Hall 1"
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Chairman: Zbigniew HANZELKA, AGH-University of Science and Technology, Poland.

Authors: Hanoch LEV-ARI, Northeastern University, USA., Aleksandar M. STANKOVIC, Tufts University, USA.

A Sparse Sampling Approach to Dynamic Sub-Cycle Decomposition of Apparent Power in General Polyphase Networks

Author: Leszek S. CZARNECKI, School of Electrical Engineering and Computer Sciences, USA.

Currents' Physical Components (CPC) in systems with semi-periodic voltages and currents

10:30 AM – 11:00 AM	Coffee Break
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11:00 AM – 1:00 PM	Lecture Session S4	Location: "Hall 1"
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Chairman: Herbert L. GINN III, University of South Carolina, USA.

ID47 ***Critical Comments on the Conservative Power Theory (CPT)***

Leszek S. Czarnecki,
School of Electrical Engineering and Computer Science, Louisiana State University, Baton Rouge, USA.

ID26 ***Budeanu's Concept of Reactive and Distortion Power Revisited***

Dimitri Jeltsema, Delft Institute of Applied Mathematics, Delft University of Technology, Delft, The Netherlands.

ID10 ***A New Reactive Power Definition Based on the Minimization of the Load Non-Reactive Currents***

Jovan Č. Mikulović, Tomislav B. Šekara,
Faculty of Electrical Engineering, University of Belgrade, Belgrade, Serbia.

ID30 ***Working and Reflected Active Powers of Three-Phase Loads***

Leszek S. Czarnecki, Tracy N. Touns,
School of Electrical Engineering and Computer Science

Louisiana State University, Baton Rouge, USA.

ID35 ***CPC Power Theory for Analysis of Arc Furnaces***

F. Martell¹, A. Izaguirre², M. Macías²,

¹Tecnológico de Monterrey, Campus Aguascalientes, Aguascalientes, México,

²Tecnológico de Monterrey, Campus Monterrey, Monterrey, México.

ID40 ***Limitations of Cross Vector Generalized p-q Theory***

Paul Haley,

School of Electrical Engineering and Computer Science, Louisiana State University, Baton Rouge, Louisiana, USA.

1:00 PM – 2:00 PM	Dinner
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2:00 PM – 3:15 PM	Lecture Session S5	Location: "Hall 1"
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Chairman: Gianfranco CHICCO, Politecnico di Torino, Italy.

ID16 ***Experimental verification of the voltage divider with auto-calibration***

D.Borkowski, J.Nabielec, A.Wetula,

AGH-UST, Krakow, Poland.

ID18 ***The Effect of System Characteristics on Very-Short-Term Load Forecasting***

Y. Loewenstern, L. Katzir, D. Shmilovitz,

Faculty of Engineering, Tel-Aviv University, Tel Aviv, Israel.

ID32 ***Online Network Impedance Identification with Wave-Package and Inter-Harmonic Signals***

M. Jordan¹, F. Grumm², H. Langkowski¹, T. Do Thanh¹, D. Schulz¹,

¹Helmut Schmidt University, Hamburg, Germany,

²Leuphana University, Lüneburg, Germany.

Chairman: Zbigniew FEDYCZAK, University of Zielona Góra, Poland.

- ID21 ***A Filter Design Approach to Maximize Ampacity of Cables in Non-sinusoidal Power Systems***
S. Sakar¹, S. H. E. Abdel Aleem², M. E. Balci³, A. F. Zobaa⁴,
¹Department of Electrical and Electronics Engineering, Gediz University- Izmir, Menemen, Izmir, Turkey,
²Mathematical, Physical and Life Sciences, 15th of May Higher Institute of Engineering, Cairo, Egypt,
³Department of Electrical and Electronics Engineering, Balıkesir University, Balıkesir, Turkey,
⁴College of Engineering, Design & Physical Sciences, Brunel University, London, Uxbridge, United Kingdom.
- ID19 ***Selective Compensation of Harmonics of Unidentified Nonlinear Loads as Ancillary Function of Grid Inverters***
J. Foulquier,
University of Applied Sciences Mittweida, Mittweida, Germany,
- ID43 ***Optimal Design of Single-Tuned Passive Filters Using Response Surface Methodology***
Selcuk Sakar¹, Aslan Deniz Karaoglan², Murat Erhan Balci³, Shady H. E. Abdel Aleem⁴, Ahmed F. Zobaa⁵,
¹Department of Electrical and Electronics Engineering, Gediz University- Izmir, Menemen, Izmir, Turkey,
²Department of Industrial Engineering, Balıkesir University, Balıkesir, Turkey,
³Department of Electrical and Electronics Engineering, Balıkesir University, Balıkesir, Turkey,
⁴Mathematical, Physical and Life Sciences, 15th of May Higher Institute of Engineering, Cairo, Egypt,
⁵College of Engineering, Design & Physical Sciences, Brunel University, London, Uxbridge, United Kingdom.
- ID22 ***Field experience with Statcom in application to wind farms***
Krzysztof Piątek¹, Andrzej Firlit¹, Daniel Wojciechowski²
¹AGH – University of Science and Technology, Cracow, Poland,
²Gdynia Maritime University, Gdynia, Poland.

3:45 PM – 4:45 PM	Lecture Session S7	Location: “Hall 1”
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Chairman: Doron SHMILOVITZ, Tel-Aviv University, Tel Aviv, Israel.

- ID36 ***Comparison of PLC-PRIME and PLC-G3 protocols***
Zbigniew Sadowski,
Institute of Electrical Engineering, University of Zielona Góra,
Poland.
- ID48 ***Communication structures and time dependency analysis in Buildings Energy Management Systems***
A. Markowski, E. Michta, R. Szulim,
University of Zielona Góra, Zielona Góra, Poland.
- ID50 ***Elastic Model of Energy Management in Micro Smart Grid***
Piotr Powroźnik, Emil Michta,
University of Zielona Góra, Zielona Góra, Poland.

3:45 PM – 4:45 PM	Lecture Session S8	Location: “Hall 2”
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Chairman: Adrian PANA, Politehnica University of Timisoara, Romania.

- ID20 ***Synchrophasor based Islanding Detection method***
S. Barcentewicz, D. Borkowski,
AGH University of Science and Technology, Cracow, Poland.
- ID42 ***Analysis of active powers of non-linear loads connected to the high voltage network***
L.I. Kovernikova,
Energy Systems Institute SB RAS, Irkutsk, Russia.
- ID44 ***Accurate CPC Power Analysis under Extreme EAF's Distortion Conditions***
A.R. Izaguirre¹, M.E. Macias¹, F. Martell².
1 Electric and Computational Department ITESM Campus Monterrey, Nuevo León, México,
2 Electric and Computational Department ITESM Campus AGS, Aguascalientes, México.

4:45 PM – 5:45 PM	Scientific Committee Meeting	Location: “Hall 1”
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7:00 PM – 11:00 PM	Gala Dinner	Location: Joannitów Castle
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Thursday, 18th June 2015

8:00 AM – 9:00 AM	Breakfast	
9:00 AM – 9:45 AM	Tutorial 5	Location: “Hall 1”
Chairman: Leszek S. CZARNECKI, Louisiana State University, USA. Author: Herbert L. GINN III, University of South Carolina, USA. Reference Signal Generators for Distributed Compensation		
9:45 PM – 10:15 PM	Coffee Break	
10:15 AM – 11:00 AM	Tutorial 6	Location: “Hall 1”
Chairman: Leszek S. CZARNECKI, Louisiana State University, USA. Authors: Jacek Kaniewski, Zbigniew Fedyczak, Paweł Szcześniak, University of Zielona Góra, Poland. AC Voltage Transforming Circuits in Power Systems		
11:00 AM – 11:30 AM	Closing Session	Location: “Hall 1”
11:30 PM – 12:30 PM	Dinner	

ISNCC 2015 Łagów, Poland Conference at a Glance

15.06.2015 Monday		16.06.2015 Tuesday			17.06.2015 Wednesday			18.06.2015 Thursday		
3:30 PM	Registration	8:00 AM	Breakfast	8:00 AM	Breakfast	8:00 AM	Breakfast	8:00 AM	Breakfast	
		8:30 AM	Welcome Address	8:30 AM	Breakfast	8:30 AM	Breakfast	8:30 AM	Breakfast	
4:00 PM	Supper	9:00 AM	Tutorial 1 Tutorial 2	9:00 AM	Tutorial 3 Tutorial 4	9:00 AM	Tutorial 5	9:00 AM	Tutorial 5	
4:30 PM		9:30 AM		CH 1		9:30 AM		CH 1		9:30 AM
5:00 PM	Registration	10:00 AM	Coffee Break	10:00 AM	Coffee Break	10:00 AM	Coffee Break	10:00 AM	Coffee Break	
5:30 PM		10:30 AM		10:30 AM		10:30 AM		10:30 AM		10:30 AM
6:00 PM	Registration	11:00 AM	Lecture Session 1	11:00 AM	Lecture Session 4	11:00 AM	Lecture Session 4	11:00 AM	End of Conference	
6:30 PM		11:30 AM		11:30 AM		11:30 AM		11:30 AM		11:30 AM
7:00 PM	Registration	12:00 AM	Dinner	12:00 AM	Dinner	12:00 AM	Dinner	12:00 AM	Dinner	
7:30 PM		12:30 PM		12:30 PM		12:30 PM		12:30 PM		12:30 PM
8:00 PM	Registration	1:00 PM	Lecture Session 2	1:00 PM	Lecture Session 2	1:00 PM	Lecture Session 2	1:00 PM	Lecture Session 2	
8:30 PM		1:30 PM		1:30 PM		1:30 PM		1:30 PM		1:30 PM
9:00 PM	Registration	2:00 PM	Coffee Break	2:00 PM	Coffee Break	2:00 PM	Coffee Break	2:00 PM	Coffee Break	
		2:30 PM		2:30 PM		2:30 PM		2:30 PM		2:30 PM
	Registration	3:00 PM	Lecture Session 3	3:00 PM	Lecture Session 3	3:00 PM	Lecture Session 3	3:00 PM	Lecture Session 3	
		3:30 PM		3:30 PM		3:30 PM		3:30 PM		3:30 PM
	Registration	4:00 AM	Lecture Session 4	4:00 AM	Lecture Session 4	4:00 AM	Lecture Session 4	4:00 AM	Lecture Session 4	
		4:15 AM		4:15 AM		4:15 AM		4:15 AM		4:15 AM
	Registration	4:45 AM	Scientific Committee Meeting	4:45 AM	Scientific Committee Meeting	4:45 AM	Scientific Committee Meeting	4:45 AM	Scientific Committee Meeting	
		5:00 PM		5:00 PM		5:00 PM		5:00 PM		5:00 PM
	Registration	5:30 AM	Gala Dinner	5:30 AM	Gala Dinner	5:30 AM	Gala Dinner	5:30 AM	Gala Dinner	
		6:00 PM		6:00 PM		6:00 PM		6:00 PM		6:00 PM
	Registration	6:30 AM	Gala Dinner	6:30 AM	Gala Dinner	6:30 AM	Gala Dinner	6:30 AM	Gala Dinner	
		6:45 AM		6:45 AM		6:45 AM		6:45 AM		6:45 AM
	Registration	7:00 PM	Gala Dinner	7:00 PM	Gala Dinner	7:00 PM	Gala Dinner	7:00 PM	Gala Dinner	
		7:30 PM		7:30 PM		7:30 PM		7:30 PM		7:30 PM

Caption: CH1 - Conference Hall 1; CH2 - Conference Hall 2; HHL - Holiday House Lesnik; JC - Joannitow Castle

